



File No. Analog.7061

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Eamon Nash GROUP: 2838
SERIAL NO: 10/774,905 EXAMINER: Matthew V. Nguyen
FILED: 2/9/04 Date of mailing
FOR: SYSTEM AND METHOD FOR REDUCING of Notice of
TRANSFER FUNCTION RIPPLE IN A Allowance: 5/5/06
LOGARITHMIC RMS-TO-DC CONVERTER

Mail Stop Issue Fee
Assistant Commissioner of Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Attn: Official Draftsman

TRANSMITTAL OF FORMAL DRAWINGS

Applicant submits herewith new drawing(s) for this application. Attached please find eight (8) sheets of formal drawings for this application.

The three month period of response set in the Notice of Allowability (PTOL 37) expires on August 7, 2006, and this submission is on or before this expiry date.

Respectfully submitted,

Matthew E. Connors
Registration No. 33,298
Gauthier & Connors, LLP
225 Franklin Street, Suite 2300
Boston, Massachusetts 02110
Telephone: (617) 426-9180
Extension 112

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.

Deborah M. Costello

Date: 7-25-06